



**Weebitnano**  
The Future Memory

**Q2 FY22:**

Executing ReRAM Commercialisation Plans

28 January 2022

# Weebit Executing ReRAM Commercialisation Plans

## Major progress made with embedded ReRAM productisation

- ◆ Technology being transferred to SkyWater's US fab, initiating qualification process
- ◆ Successfully demonstrated Weebit's embedded ReRAM technology at 28nm
- ◆ Received first silicon demo wafers integrating Weebit's embedded ReRAM module
- ◆ Raised \$35.2 million to accelerate growth initiatives and technology development
- ◆ Commenced environmental initiative with CEA-Leti to measure carbon footprint
- ◆ Filed five new patents, further protecting intellectual property
- ◆ Post-quarter end, demonstrated its first crossbar ReRAM arrays
- ◆ Advancing commercial negotiations with several potential partners and customers



# Technology Being Transferred to SkyWater's Fab

## Qualification process currently on track

- ◆ The transfer of Weebit's embedded ReRAM technology to SkyWater's US production fab is progressing
- ◆ First prototype tape-out expected to be completed by mid-year, followed by technology qualification
- ◆ Once qualified, SkyWater customers who license Weebit's technology will be able to embed it within new product designs and mass-produce at SkyWater's fab
- ◆ Current over-demand for semiconductor products combined with the current COVID-19 situation is causing some uncertainties
  - ◆ Weebit and its suppliers are working hard to minimise any potential impact to Weebit's qualification schedule

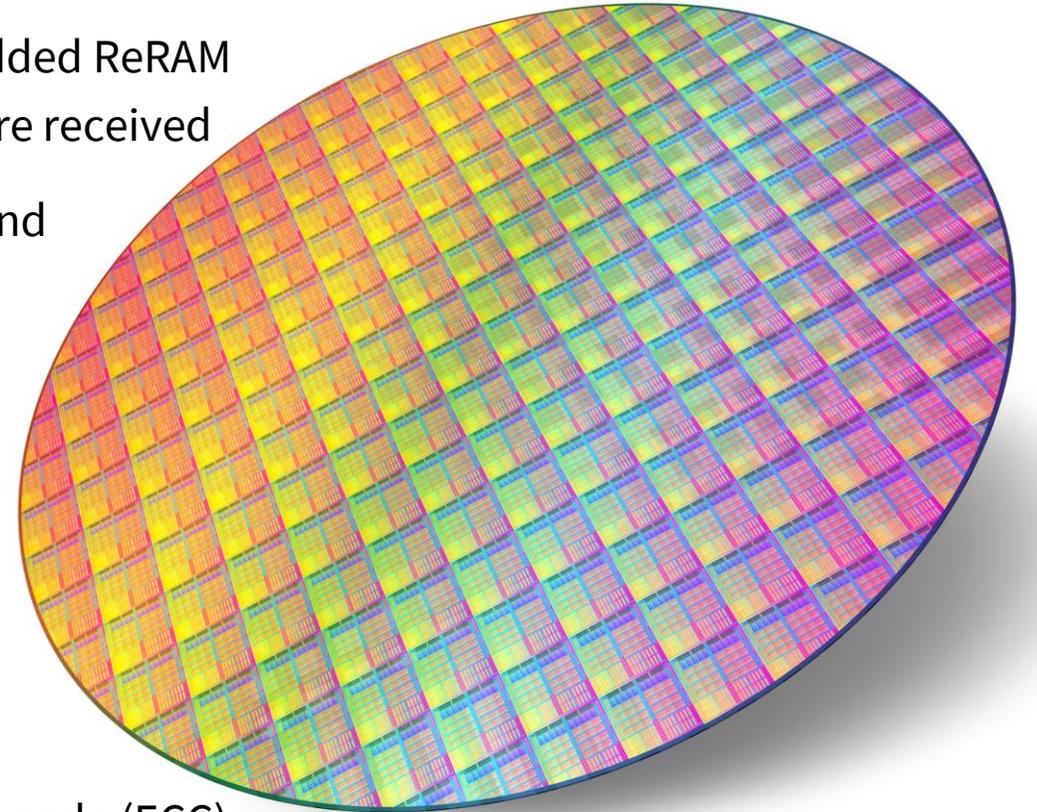
## Technology qualification is a major milestone for Weebit's commercialisation



# First Silicon Demo Wafer Received

## Wafers integrate WBT's embedded ReRAM module with complete subsystem

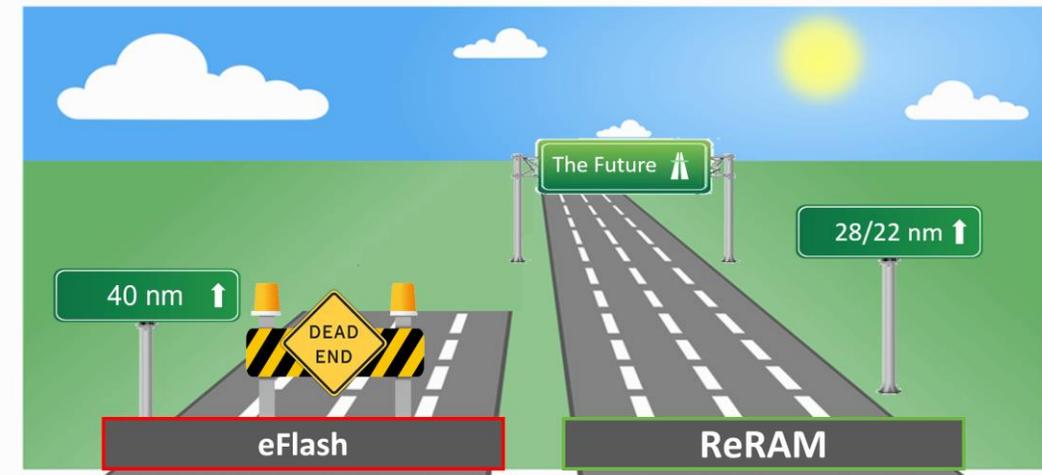
- ◆ First manufactured silicon wafers integrating Weebit's embedded ReRAM module inside a complete subsystem demonstration chip were received
- ◆ Demo chips enable potential customers to run applications and test Weebit's technology ahead of commercial orders and volume production
- ◆ Demo wafers are now being sliced into chips, packaged, tested, characterised and qualified
- ◆ Weebit's embedded ReRAM module has been designed with unique patent-pending analog and digital smart circuitry and includes a 128Kb array, control logic, decoders, IOs (input/output communication elements) and error correcting code (ECC)



# Successful Demonstration at 28nm

## A key step in productising the technology for the embedded memory market

- ◆ Weebit and development partner CEA-Leti demonstrated excellent results of Weebit's ReRAM technology at 28nm on 300mm wafers
  - ◆ A key step towards productisation of embedded Non-Volatile Memory (NVM) for AI, autonomous driving, 5G and advanced IoT
  - ◆ Testing showed very good endurance and data retention alongside other production-level parameters
- ◆ The 28nm geometry is very popular and widely used in a range of advanced embedded applications
  - ◆ Mark Liu, Chairman of TSMC, the world's largest fab, recently called 28nm –  
*“The sweet spot for our embedded memory applications..”*
- ◆ Weebit's ability to support smaller geometries expands its range of potential industries and applications



# Secured \$35.2m to Accelerate Growth

## Introduced major Israeli institutional investors onto the register

- ◆ Raised \$35.2 million via a Placement to four Israel-based institutional and pension funds and an Entitlement Offer to existing shareholders
  - ◆ Additional \$300K to be invested by 3 directors after EGM approval
- ◆ Meitav Dash, a major institutional investment and pension fund in Israel, was the Placement's cornerstone investor
  - ◆ This was a key milestone, representing the first time international institutional investors joined Weebit's register
- ◆ The Entitlement Offer for existing shareholders was also oversubscribed
- ◆ Funding to underpin accelerated growth, supporting pursuit of business opportunities, research and development in embedded and discrete projects, and general working capital requirements

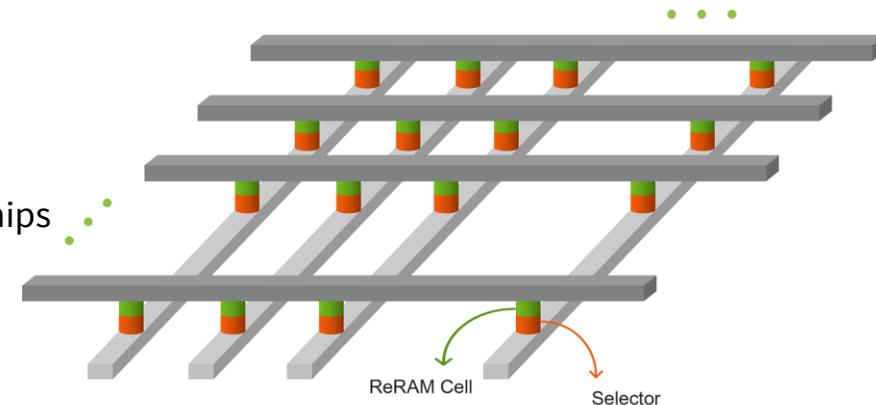
# MEITAV DASH.

# Other Q2 Developments & Post-Q2 Updates

- ◆ Commenced environmental initiative with CEA-Leti to measure carbon footprint
  - ◆ Weebit and CEA-Leti commenced a Life Cycle Assessment (LCA) of Weebit's ReRAM technology to analyse and quantify its environmental impact compared to other NVMs
  - ◆ This will assist in identifying ways to reduce the environmental impact of AI and IoT, which are significant contributors to emissions
- ◆ Filed 5 new patents, further protecting intellectual property
- ◆ Advanced commercial negotiations with several potential partners and customers

## Post-quarter end updates

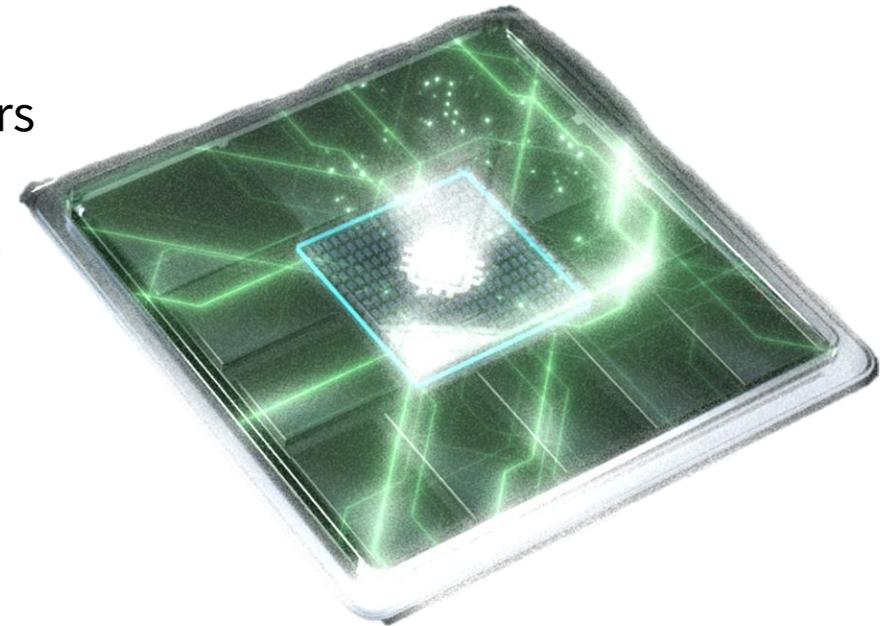
- ◆ WBT and CEA/Leti demonstrated its first crossbar ReRAM arrays
  - ◆ Crossbar arrays combine WBT's ReRAM technology with a selector – This is a key milestone on the Company's path to creating discrete (stand-alone) NVM chips
- ◆ Published a research paper on neuromorphic computing with IIT Delhi



# Looking Ahead

## Weebit plans to achieve the following milestones by the end of CY22

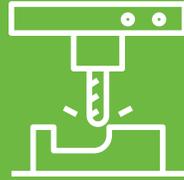
- ◆ Transferring and qualifying its technology at SkyWater's US fab
- ◆ Providing functional test results of its embedded ReRAM module and demonstrating its functionality and performance
- ◆ Signing new licensing agreements with partners and customers
- ◆ Continuing scaling down its technology to smaller geometries



# Key Takeaways



Weebit's leading ReRAM (NVM) solution can replace Flash memory



Technology now transferred to a production fab



Signed first licensing deal with SkyWater Technologies. Ongoing discussions with other customers



Renowned Board & management team with extensive semiconductor commercialisation experience



Demonstrated combined ReRAM cell with OTS selector for discrete memory Market (Industry's 1st)

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A close-up, artistic photograph of a microchip or circuit board. The chip is illuminated with vibrant green and blue light, creating a futuristic, high-tech atmosphere. Numerous thin, vertical lines of light, resembling fiber optic cables or data streams, extend upwards from the chip, adding to the sense of connectivity and data flow. The background is dark, making the illuminated components stand out prominently.

# Thank You!

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