

# Q2 FY21 Quarterly Update

29 January 2021



# Who We Are?



## Weebit Nano

Developer of ReRAM memory technology, (significantly faster and more energy efficient, higher endurance) than existing Flash memory technology



## Competitive advantage

Uses standard materials and equipment in fabrication, very cost-competitive

**THE ONLY**  
SiOx  
ReRAM  
vendor\*



## Technology maturity

Ready for technology transfer to production.  
Demonstrated high retention and endurance



## Rapid market growth

NVM market estimated at US\$60B, growing to over US\$100B by 2025 (MarketsandMarkets Research)



## Market traction

Ongoing discussion with potential semiconductor customers and partners; targeting first commercial agreements by mid 2021



## Team

World class leadership team in place to support transition to commercialisation

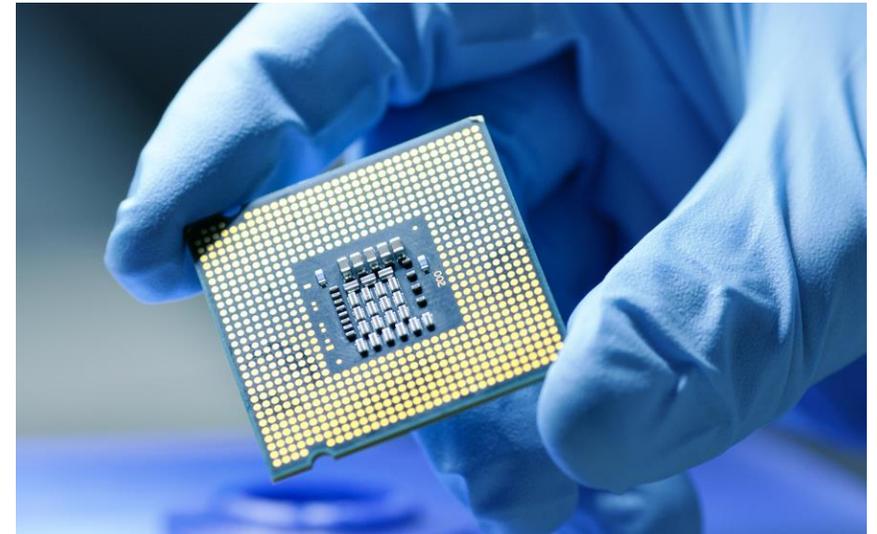
\* Weebit is active in all leading semiconductor markets and has not observed any other vendor that offers, or has announced an intention to offer, ReRAM technology using SiOx only without having to add any non-standard materials

# KEY HIGHLIGHTS FOR Q2 FY21

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## ANOTHER PRODUCTIVE QUARTER AS WBT PREPARES FOR PRODUCTISATION

- Completed the stabilisation process with Leti; technology now ready to transfer to a production fab; discussions with potential production partners advancing
- Expanded strategic partnership with Leti to incorporate additional activities across ReRAM technology development, embedded and standalone memory markets
- Raised \$15 million via heavily oversubscribed Placement and Share Purchase Plan
- Further strengthened executive team with the appointments of Ilan Sever as Vice President Research & Development and (post quarter end) Ishai Naveh as Chief Technology Officer
- Filed two new patents with Leti, optimising ReRAM performance
- Included in the S&P/ASX Technology Index



# KEY APPOINTMENTS MADE

## FURTHER STRENGTHENED MANAGEMENT TEAM



- Non-volatile memory veteran Ishai Naveh appointed as Chief Technology Officer on 12 January (post balance date)
- More than 35 years' experience in the semiconductor field, ranging from process development and integration, silicon manufacturing, setting technology requirements and directing technical teams to address customers' needs
- Serving at Tower Semiconductor since 1993, he was responsible for NVM and Foundry Technologies. In 2007 Mr Naveh co-founded Adesto, one of the early ReRAM companies, leading technology and product definition and adaptation for market requirements
- Will drive the strategic direction of the Company's technology development



- Ilan Sever appointed as Vice President Research & Development to drive the Company's progress within the embedded, standalone and neuromorphic markets
- Brings more than 25 years' in Very Large-Scale Integration (VLSI) design with extensive experience across the entire semiconductor spectrum, from complex mixed-signal System-on-a-Chip (SOC) design through to IoT and security chips as well as physical device and process improvements
- An expert in both non-volatile and volatile memories, Ilan's previous roles include Group CTO at semiconductor design and solutions company Dolphin Integration, Director of IP, Library & Design Services at Tower Semiconductor and Design Centre Manager at ST Microelectronics

# WORLD-RENOWNED BOARD OF DIRECTORS



**David Perlmutter**  
CHAIRMAN

Served as Executive Vice President and General Manager at the Intel Architecture Group (IAG) and Chief Product Officer of Intel Corporation

IEEE Fellow and winner of the prize for innovation in industrial development from the Israeli President

Serves on the board of Mellanox as well as several other semiconductor startups



**Coby Hanoch**  
CEO

40 years of experience in the semiconductor industry, including 2 exits at Verisity Design and Jasper Design Automation

CEO of PacketLight, turned it around from the verge of bankruptcy

Leading WW sales teams for almost 25 years

Appointed CEO in Oct 2017



**Dr. Yoav Nissan-Cohen**  
EXEC.  
DIRECTOR

Received his PhD researching non-volatile memories, under the supervision of Prof Dov Frohman, the inventor of the first non-volatile memory technology

Led the creation of Tower Semiconductor and was its CEO for almost 10 years, including taking it public on NASDAQ

Co-founder of Saifun Semiconductor, a NVM company which was subsequently sold to Spansion



**Atiq Raza**  
NON-EXEC.  
DIRECTOR

Over 40 years experience in the semiconductor industry

Chairman and CEO of NexGen Inc for 7.5 years, and then became President and COO of Advanced Micro Devices (AMD) after NextGen was acquired by AMD

Held multiple roles as investor, Chairman and CEO of several semiconductor companies



**Fred Bart**  
NON-EXEC.  
DIRECTOR

Extensive track record of business success with decades of experience across multiple industries

Chairman and major shareholder of Electro Optics Systems Limited (EOS), Chairman of Audio Pixels Holdings Limited (AKP) and holds a wide range of private companies worldwide



**Ashley Krongold**  
NON-EXEC.  
DIRECTOR

Over 15 years experience in the investment banking and accounting industry

CEO of the Krongold Group. He serves on the Boards and is a Director of various ASX listed companies, communal charities, foundations and organizations globally.

Member of YPO (Young Presidents' Organization)





# LETI AGREEMENT EXTENSION

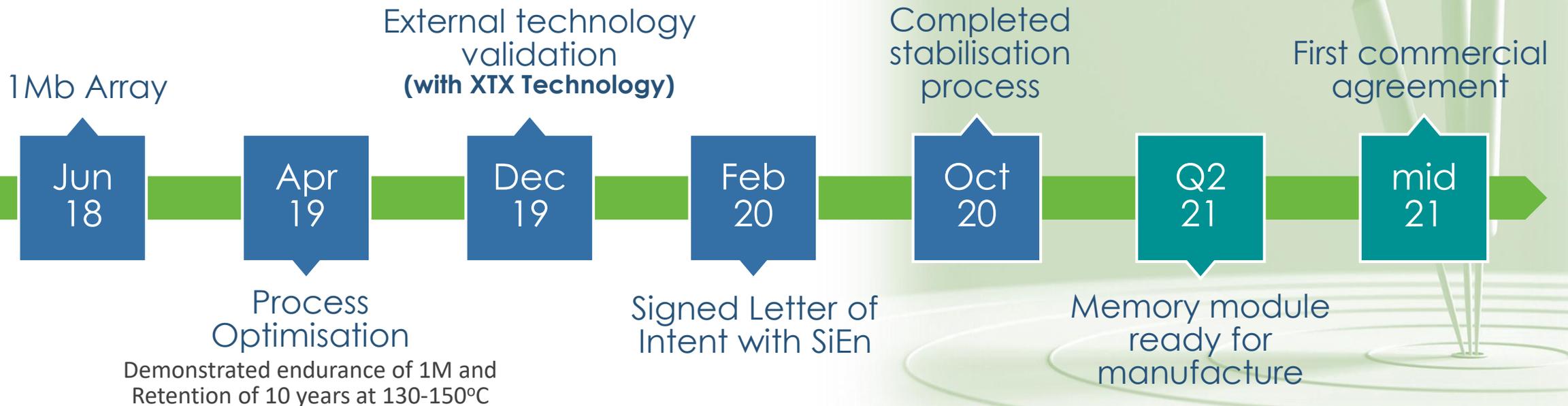
## LONGSTANDING PARTNERSHIP BROADENED TO ADDRESS MARKET NEEDS

- **Weebit and Leti recently extended partnership to help progress and accelerate the commercialisation process. Agreement extension covers:**
  - 1. Further enhancements to Weebit's ReRAM technology**
    - Further enhancing the efficiency and robustness of Weebit's ReRAM technology to ensure its technology remains industry-leading and attractive to potential customers and partners
  - 2. Enhancing the embedded memory module**
    - Adding enhanced functionality to better address customer needs and increase attractiveness to potential customers
      - This decision followed discussions and input from industry experts and partners
    - Weebit's ReRAM memory module will be one of the first in the world to be integrated into a sub-system consisting of a processor, Static Random-Access Memory (SRAM), the ReRAM array and peripherals
    - Expected to slightly push out the completion of the memory module design stage to June 2021
  - 3. Development of the selector for the standalone memory market**
    - Continuing as planned, with the goal of demonstrating a working combined cell by Sep 2021



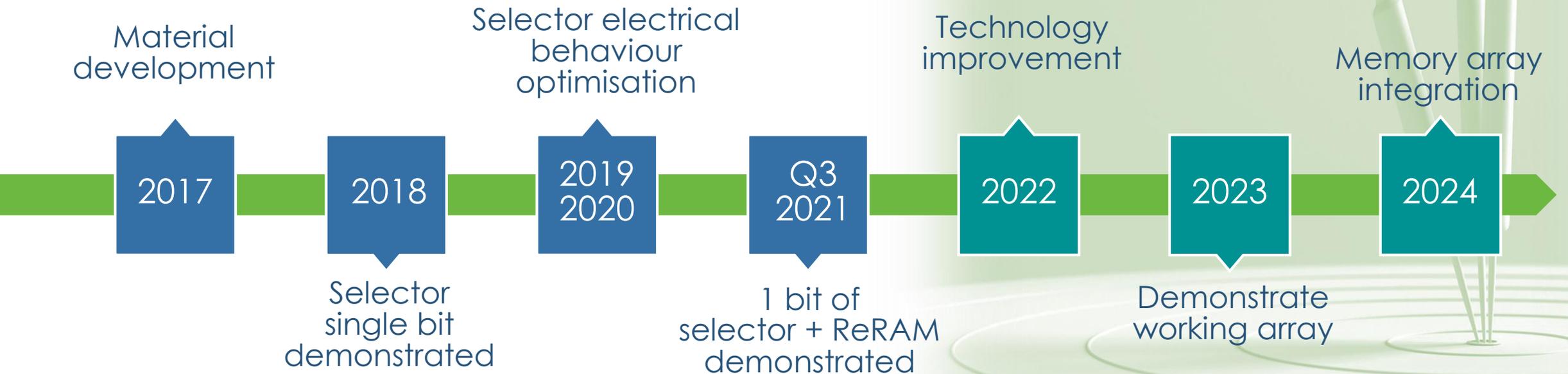
# PROGRESSING TO COMMERCIALISATION

## Embedded Memory IP Roadmap



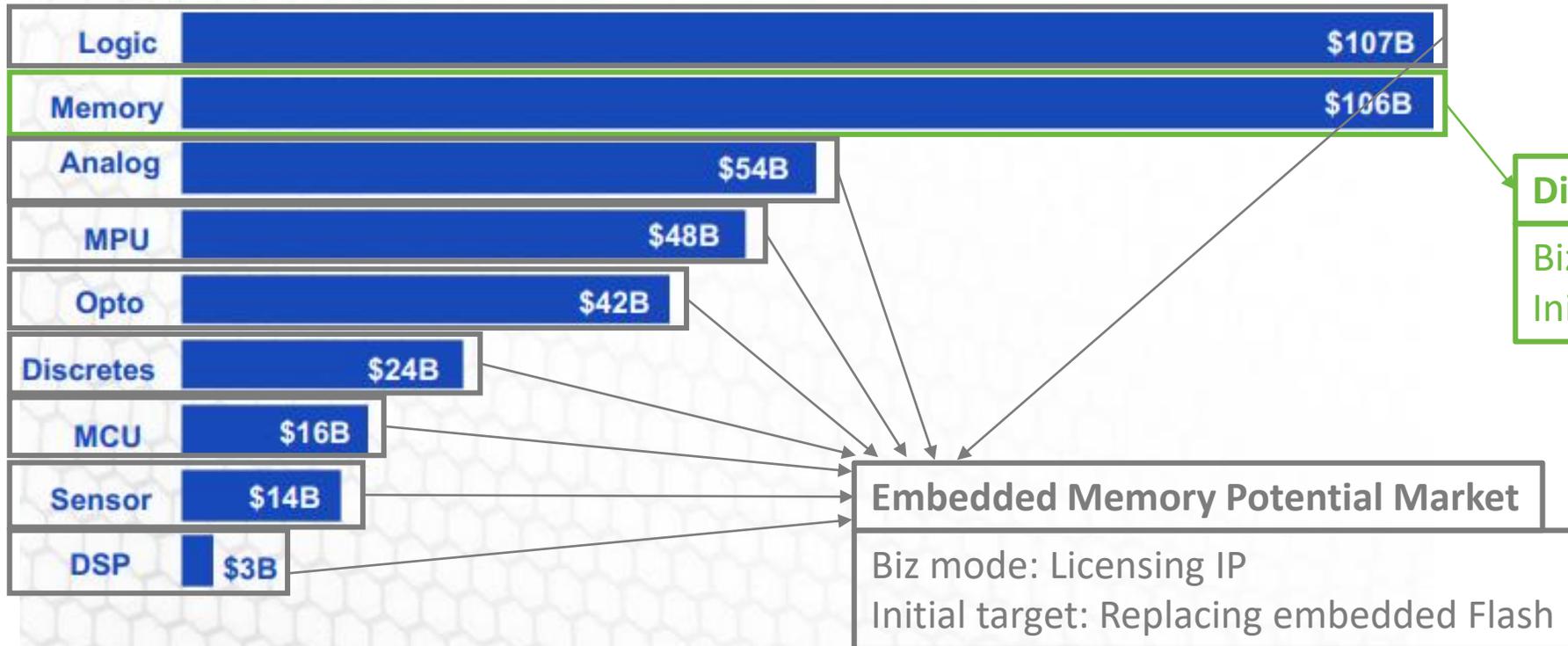
# PROGRESSING TO COMMERCIALISATION

## Discrete Memory Chip Roadmap



# MASSIVE MARKET POTENTIAL

Distribution of Worldwide Semiconductor Sales By Product Segment 2019



Source: World Semiconductor Trade Statistics (WSTS) and SIA Estimates.

# COVID-19 IMPACT

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- **The safety and health of our employees, partners and contractors is of top priority**
  - We have taken measures to enable continuous operation, mostly from home
  - Manufacturing and testing continue uninterrupted, while appreciating social distancing and other restrictions
- **Travel restrictions continue to affect us**
  - We cannot send engineers to potential fabs and partners for demonstration purposes
  - We cannot meet Leti, our research partner, for ongoing work and our annual planning week
  - All business travels are replaced with video conferencing, which is less effective
  - Subject to COVID-19 travel restrictions not creating any delays, we are still aiming to secure a first commercial agreement by mid-2021
  - Impacted communications between the Israeli and French R&D teams might slightly impact the memory module milestone date of June-21

Questions



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