

Weebit Nano appoints Ilan Sever to drive R&D

Expert in non-volatile memory design & integration

16 November 2020 – Weebit Nano Ltd (ASX: WBT), a leading developer of next generation memory technology for the global semiconductor industry, has appointed non-volatile memory design expert Mr Ilan Sever as Vice President Research & Development to drive the Company's progress within the embedded, standalone and neuromorphic markets. Mr Sever will commence in the role on 24 November 2020.

Mr Sever brings more than 25 years' in Very Large-Scale Integration (VLSI) design to Weebit Nano with extensive experience across the entire semiconductor spectrum, from complex mixed-signal System-on-a-Chip (SOC's) design through to IoT and security chips as well as physical device and process improvements.

An expert in both non-volatile and volatile memories, Mr Sever's previous roles include Group Chief Technology Officer (CTO) at semiconductor design and solutions company Dolphin Integration, Director of IP, Library & Design Services at Tower Semiconductor and Design Centre Manager at ST Microelectronics.

As Weebit Nano nears productisation and first commercial agreements, Mr Sever will oversee the embedded memory module design and its adaptation to meet the specific customer requirements. He will also lead the development of innovative new products for the standalone memory markets, including managing the development of the standalone memory chip and taking it to mass production.

Coby Hanoch, CEO of Weebit Nano, said: "Ilan's extensive knowledge across all aspects of semiconductor design will be instrumental during a pivotal period for the Company, as we continue to productise our ReRAM technology and prepare for the transfer to a production fab. His extensive experience leading engineering teams and proven ability to build local and global partnerships with industrial partners will be key to progressing the development and commercialisation of our embedded and standalone memory solutions."

Ilan Sever said: "I have been following Weebit for several years and know its management team, and am very glad to finally join the Company as it transitions from its research origins towards productisation and commercialisation. Weebit's innovative silicon oxide ReRAM technology has many competitive advantages and is well positioned to replace Flash memory in millions of applications across numerous industries. The addressable market and product opportunities for Weebit's non-volatile memory is significant and I'm looking forward to getting the technology out there."

This announcement has been authorised for release by the Board.

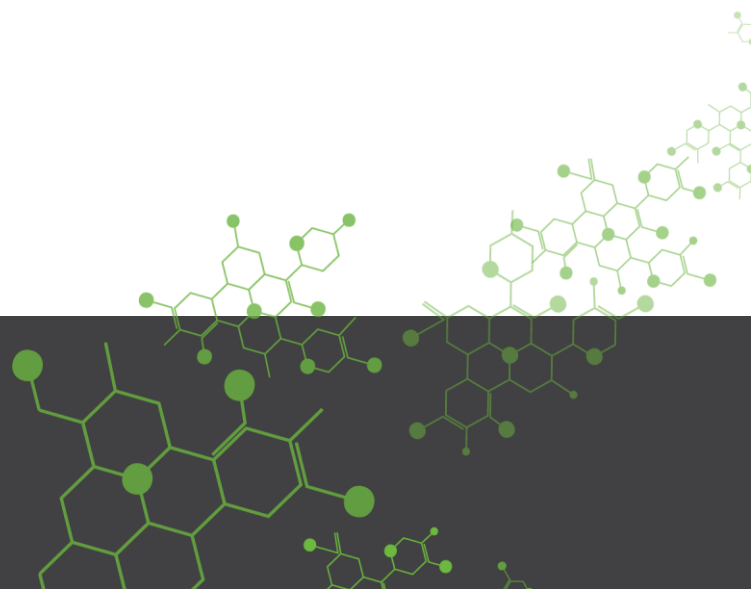


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About Weebit Nano Limited

Weebit Nano is a leader in the development of next generation computer memory technology, and plans to become the new industry standard in this space. Its goal is to address the growing need for a significantly higher performance and lower power computer memory technology. Weebit Nano's ReRAM technology is based on fab-friendly Silicon Oxide, allowing the company to rapidly execute, without the need for special equipment or preparations. The company secured several patents to ensure optimal commercial and legal protection for its ground-breaking technology.

Weebit Nano's technology enables a quantum leap, allowing semiconductor memory elements to be significantly cheaper, faster, more reliable and more energy efficient than the existing Flash technology. Weebit Nano has signed an R&D agreement with Leti, an R&D institute that specialises in nanotechnologies, to further develop SiOx ReRAM technology.

For more information please visit: <http://www.weebit-nano.com/>



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