

## Weebit Nano achieves 40nm working SiOx ReRAM cells milestone one month ahead of schedule

27 Nov, 2017 – Weebit Nano (ASX: WBT), an Israel-based semiconductor company developing the next generation of memory technology, is pleased to report it has reached the 40nm working cells milestone one month ahead of schedule.

Measurements performed on 40nm memory cells on various wafers verified the ability of Weebit Nano SiOx ReRAM cells to maintain its memory behaviour in accordance with previous experiments performed on 300nm cells. This is a significant milestone for Weebit Nano as it verifies that its technology can be scaled down to advanced geometries used in a wide variety of applications while maintaining its benefits.

The 40nm size is of particular importance as it is a common existing technology node used in many industrial applications such as mobile phones, laptops and wearable technology used in the ‘Internet of Things’, and will help to progress potential co-operation with industry players.

**Coby Hanoch, CEO of Weebit Nano**, said: “Reaching the 40nm milestone secures our position among the leading ReRAM companies. Demonstrating such an advanced geometry paves the way for us to further develop our technology by scaling up array capacities for use in different applications and integrating into deeply scaled down CMOS technologies that are used in all processors, RAM and digital logic circuits on most small devices.”

“The 40nm cell characterisation results were achieved ahead of schedule due to the excellent co-operation between Weebit Nano and Leti’s R&D team, leveraging the very strong engineering integration capabilities of Leti’s pre-industrialisation facilities in Grenoble, France”, said Mr Hanoch.

**David Perlmutter, Chairman of the Board of Weebit Nano**, said: “Our development strategy, which focuses on unique IP that capitalises on known manufacturing processes, is paying off with fast and flawless execution. What took other companies up to 10 years to achieve, took Weebit Nano less than two years, thanks to our unique approach of focusing on standard Silicon Oxide which does not require any special equipment or processes.”

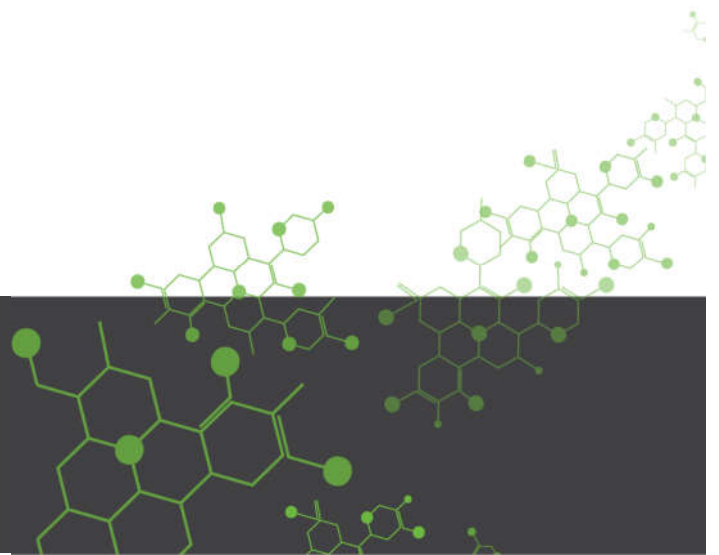
Over the next few months Weebit Nano will continue extensive characterisation of its 40nm SiOx ReRAM cells performance and will start scaling up its array capacity into Kb and Mb arrays structures during the first half of 2018.

In parallel to the 40nm SiOx ReRAM cell characterisation, Weebit Nano is working to define its technology roadmap beyond 40nm and future industry collaborations.



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**About Weebit Nano Limited**

Weebit Nano is a leader in the development of next generation computer memory storage, and plans to become the new industry standard in this space. Its goal is to address the growing need for a significantly higher performance and lower power data storage technology. Weebit Nano's ReRAM technology is based on fab-friendly Silicon Oxide, allowing the company to rapidly execute, without the need for special equipment or preparations. The company secured several patents to ensure optimal commercial and legal protection for its ground-breaking technology.

Weebit Nano's technology enables a quantum leap, allowing semiconductor memory elements to be significantly cheaper, faster, more reliable and more energy efficient than the existing Flash technology. Weebit Nano has signed an R&D agreement with Leti, an R&D institute that specialises in nanotechnologies, to further develop SiOx ReRAM technology.

For more information please visit: <http://www.weebit-nano.com/>



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